

| Notice of Allowability | Application No. | Applicant(s) |
|--|---|----------------------|
| | 10/531.396 | HEIJNA, ROELAND JOHN |
| | Examiner | Art Unit |
| | Long Nguyen | 2816 |
| The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. | | |
| 1. X This communication is responsive to the pre-amendment on 4/14/05 and the authorization for Examiner's Amendment on 9/27/06. | | |
| 2. The allowed claim(s) is/are <u>1-6</u> . | | |
| 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) | | |
| Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material | 5. ☐ Notice of Informal Pa 6. ☐ Interview Summary Paper No./Mail Dat 7. ☑ Examiner's Amendn 8. ☑ Examiner's Stateme 9. ☐ Other | (PTO-413), e |
| | | |

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EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Peter Zawilski (Reg. No. 43,305) on 9/27/06.

The application has been amended as follows:

In The Abstract

The abstract has been replaced with the following abstract.

--Consistent with an example embodiment, there is a detector for detecting a differential binary signal having a first signal level during a first period and a second signal level during a second period. The detector comprise an offset circuit for producing a slice level offset signal in response to a detected binary signal, an amplitude signal and a slice level signal. The differential binary signal is detected using the first signal level during the first period and using the second signal level during the second period.--

In The Claims

Claim 1, line 5, "the amplitude" has been changed to --an amplitude--.

Claim 1, line 5, "the binary" has been changed to --the differential binary--.

Claim 1, line 7, "the average" has been changed to -- an average--.

Claim 1, line 7, "the binary" has been changed to --the differential binary--.

Claim 1, line 8, "the detected" has been changed to --a detected differential--.

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Claim 1, line 10, "outputted" has been changed to --detected differential--.

Claim 1, line 12, "binary" has been changed to --differential binary--.

Claim 1, line 13, "wherein said" has been changed to --wherein all of said--.

Claim 1, line 14, "a first slice level" has been changed to --the first signal level--.

Claim 1, line 14, "a second slice level" has been changed to -- the second signal level--.

Claim 1, line 15, "all said" has been changed -- all of said--.

Claim 3, line 3, "and/or" has been changed to --and--.

Claim 4, line 2, "the binary" has been changed to --the differential binary--.

Claim 4, line 3, "it to the other circuits." has been changed to --the differential binary to the level shift circuit.--.

Claim 5, line 1, "An offset circuit for use in a" has been changed to -- A--.

Claim 5, ,line 2, "the offset" has been changed to --wherein the offset--.

Claim 5, line 2, "comprising:" has been changed to --comprises:--.

Claim 6, line 1, "An offset circuit" has been changed to -- A detector--.

Reasons For The Above Changes

The above changes in the claims have been made to overcome the minor informalities and/or indefinite problems in the claims. The abstract is rewritten so that it complies with the U.S. format.

REASONS FOR ALLOWANCE

2. Claims 1-6 are allowed.

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Claim 1 is allowed because the prior art of record fails to discloses or suggest a detector comprising an amplitude detection signal, a slice level detection circuit, an output circuit, an offset circuit and a level shift circuit with the recited connections and operation set forth therein. In particularly, the prior art of record fails to disclose or suggest that the detector including, in combination with other limitations, an offset circuit for producing a slice level offset signal in response to the detected differential binary signal, the slice level signal and the amplitude signal, and a level shift circuit coupled to the output circuit for level shifting the differential binary signal in response to the slice level offset signal.

Claims 2-6 are allowed because they depend on claim 1.

Conclusion

- 3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
- 4. Any inquiry concerning this communication or earlier communications from the examiner should be directly to Examiner Long Nguyen whose telephone number is (571) 272-1753. The Examiner can normally be reached on Monday to Thursday from 8:00am to 6:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tim Callahan, can be reached at (571) 272-1740. The fax number for this group is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR

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LONG NGUYEN
PRIMARY EXAMINER

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